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**** CONTINUING DATA** *YES PD*
 This application is a 371 of PCT/JP03/06860 05/30/2003

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 JAPAN 2002-209650 07/18/2002
 JAPAN 2002-209639 07/18/2002
 JAPAN 2002-259291 09/04/2002
 JAPAN 2002-259284 09/04/2002
 JAPAN 2002-324238 11/07/2002

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 7	TOTAL CLAIMS 59	INDEPENDENT CLAIMS 5
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	Verified and Acknowledged <i>Laughlin</i> Examiner's Signature	<i>PD</i> Initials		

ADDRESS
20457

TITLE
Multilayer wiring board, method for producing the same, semiconductor device and radio electronic device

FILING FEE RECEIVED 3250	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees
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